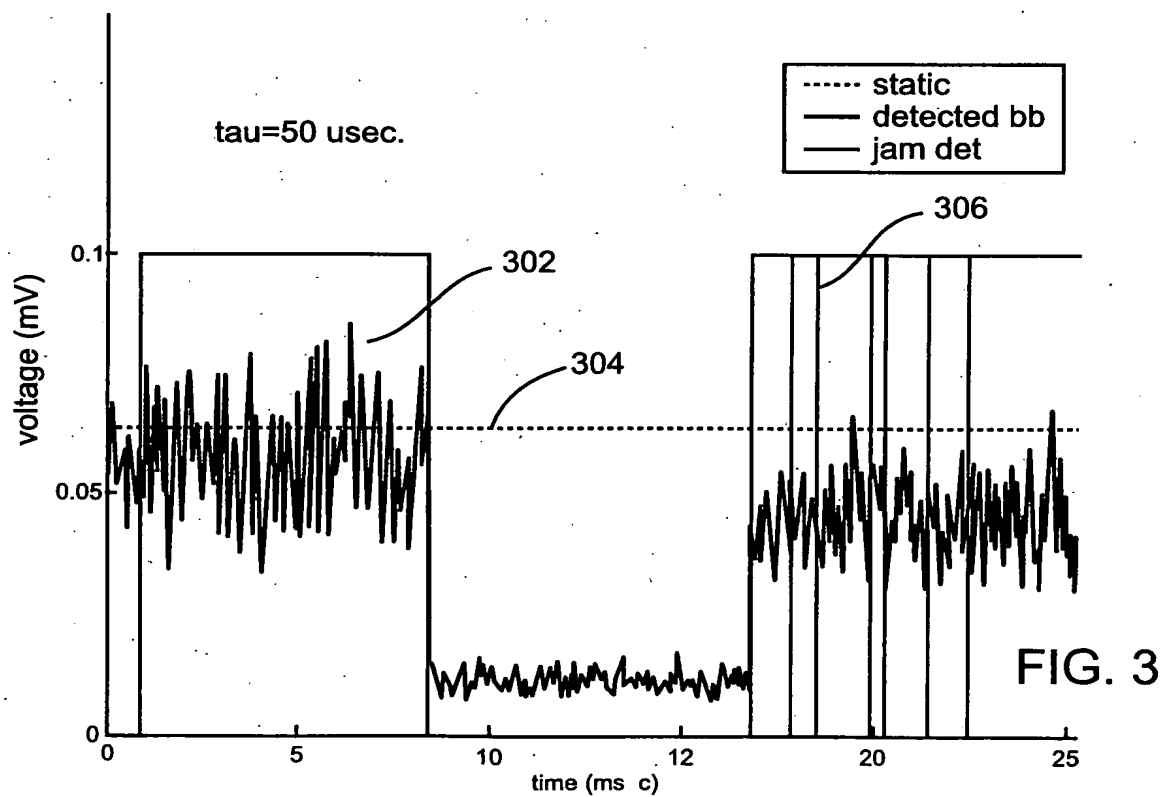
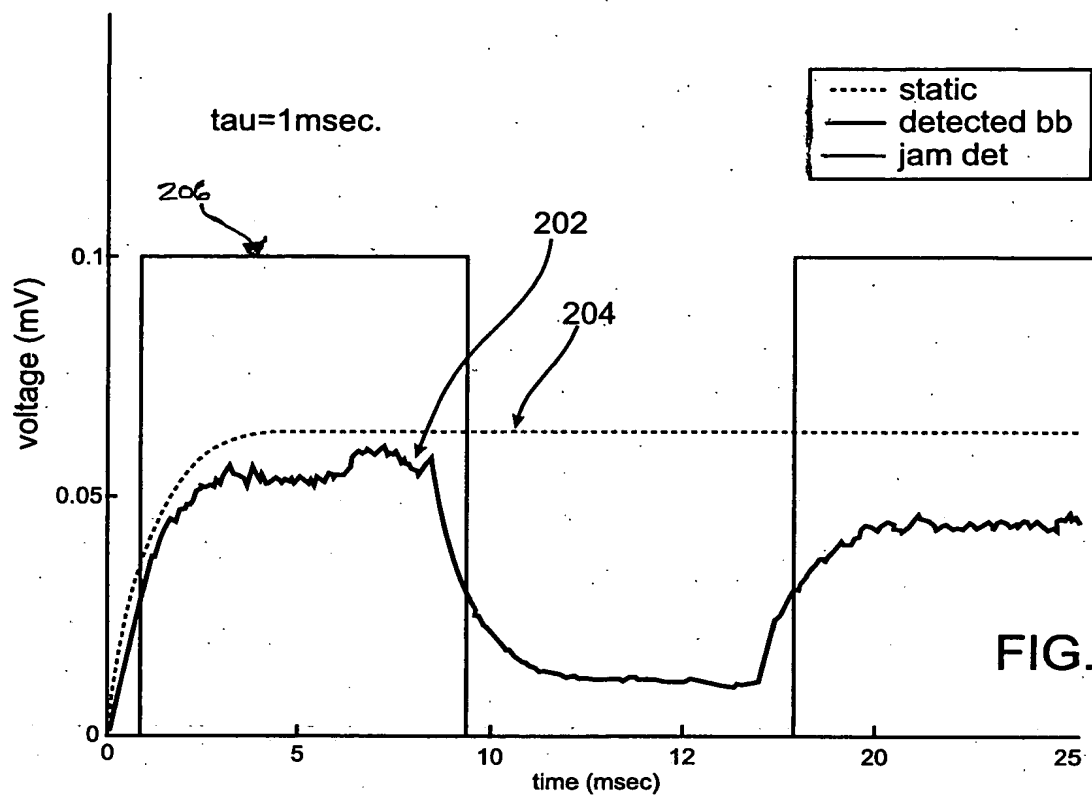


FIG. 1



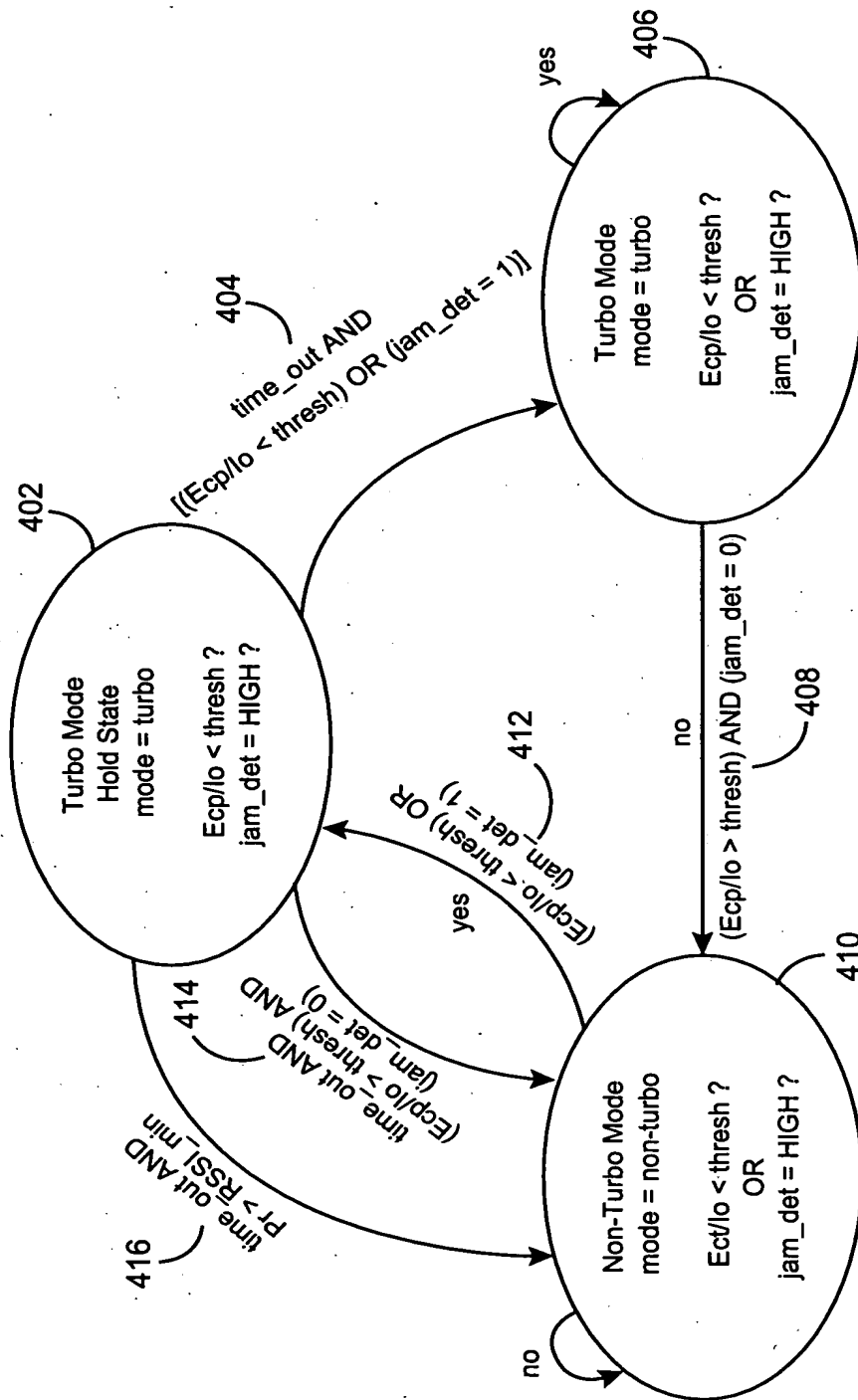


FIG. 4

Up Bits > 50%	Jam_det	Turbo Mode
0	0	0
0	1	1
1	0	1
0	1	1

FIG. 5

Jammer Detector Output (1-bit A/D)	Signal Strength (RSSI)	Ecp/Io	Linearity	LPF state
Low	Weak	X	Low	matched
Low	Strong	Low	Low	matched
Low	Strong	High	Low	low ICI
High	Weak	X	High	matched
High	Strong	Low	High	matched
High	Strong	High	Low	low ICI

FIG. 6

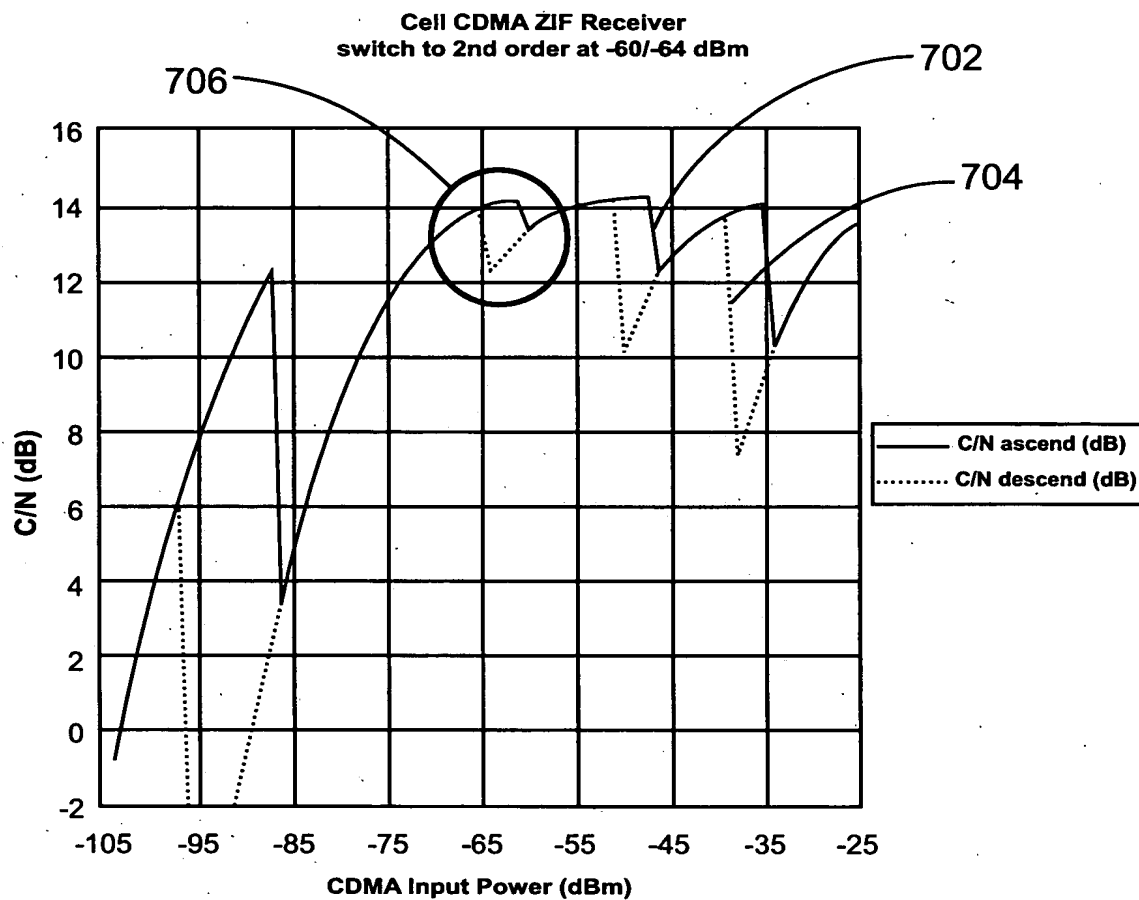


FIG. 7

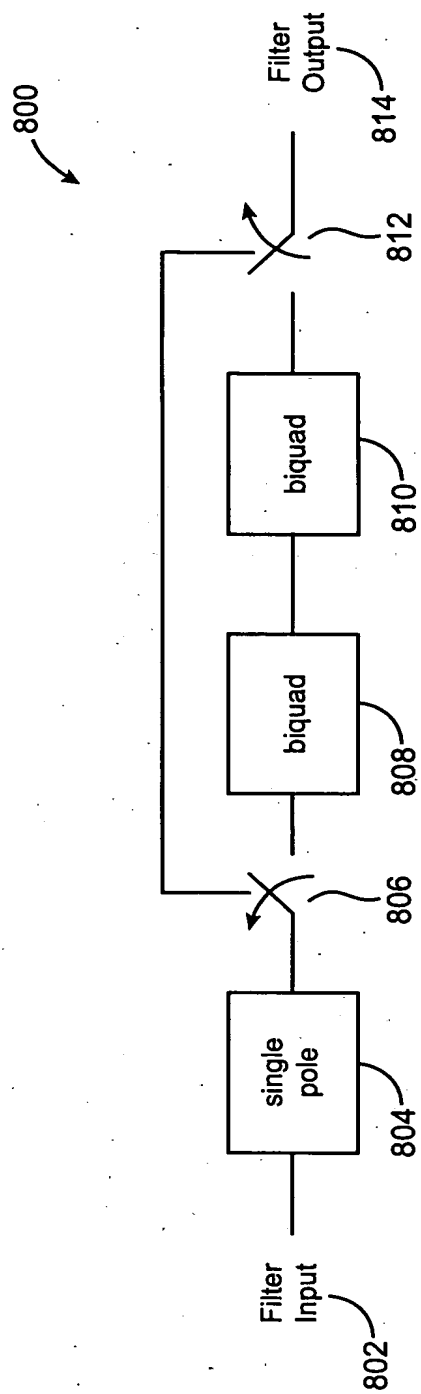


FIG. 8

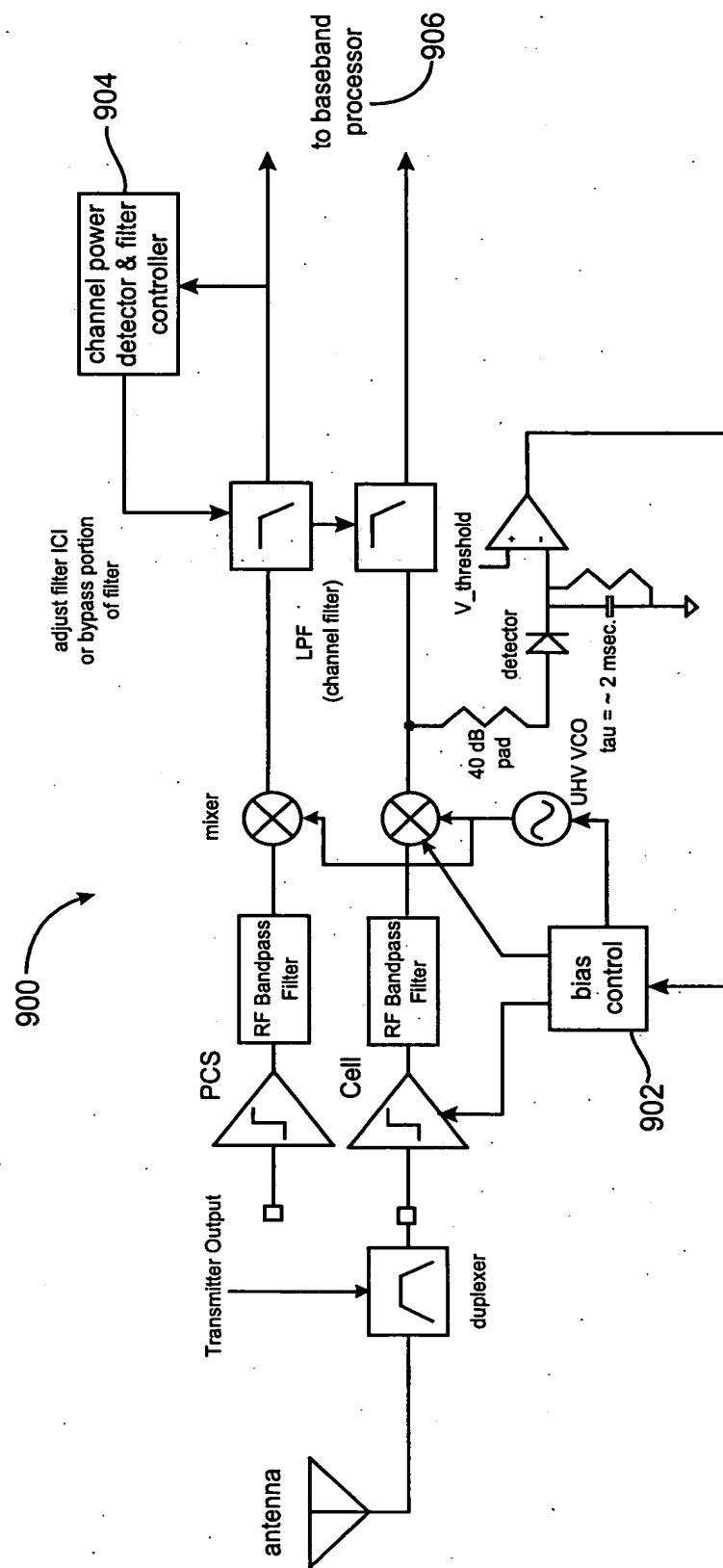


FIG. 9